

**FIG. 1**

FIG. 2 is a block diagram of a protocol processor element 200.

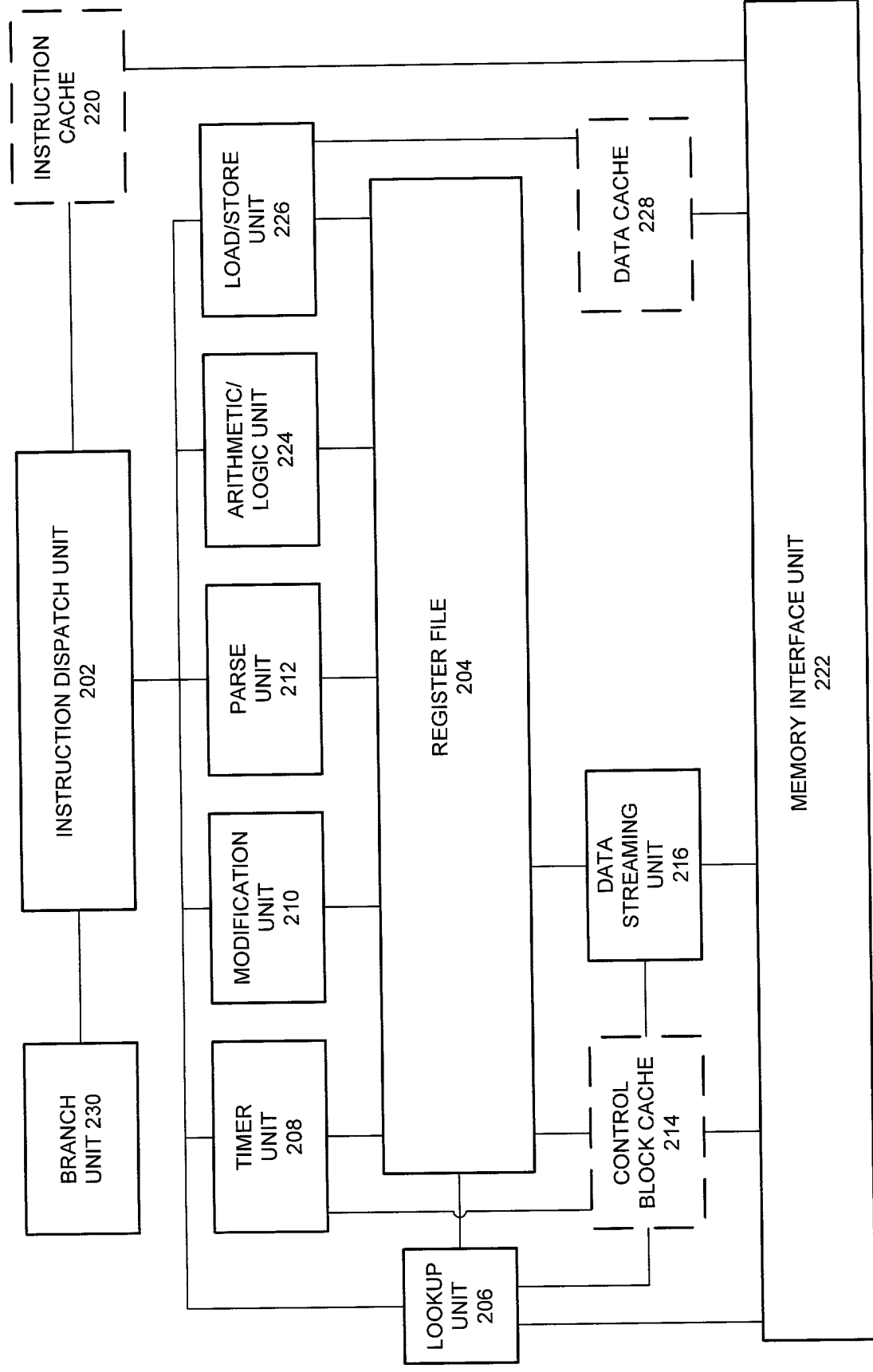


FIG. 2

FIG. 2

FIG. 2

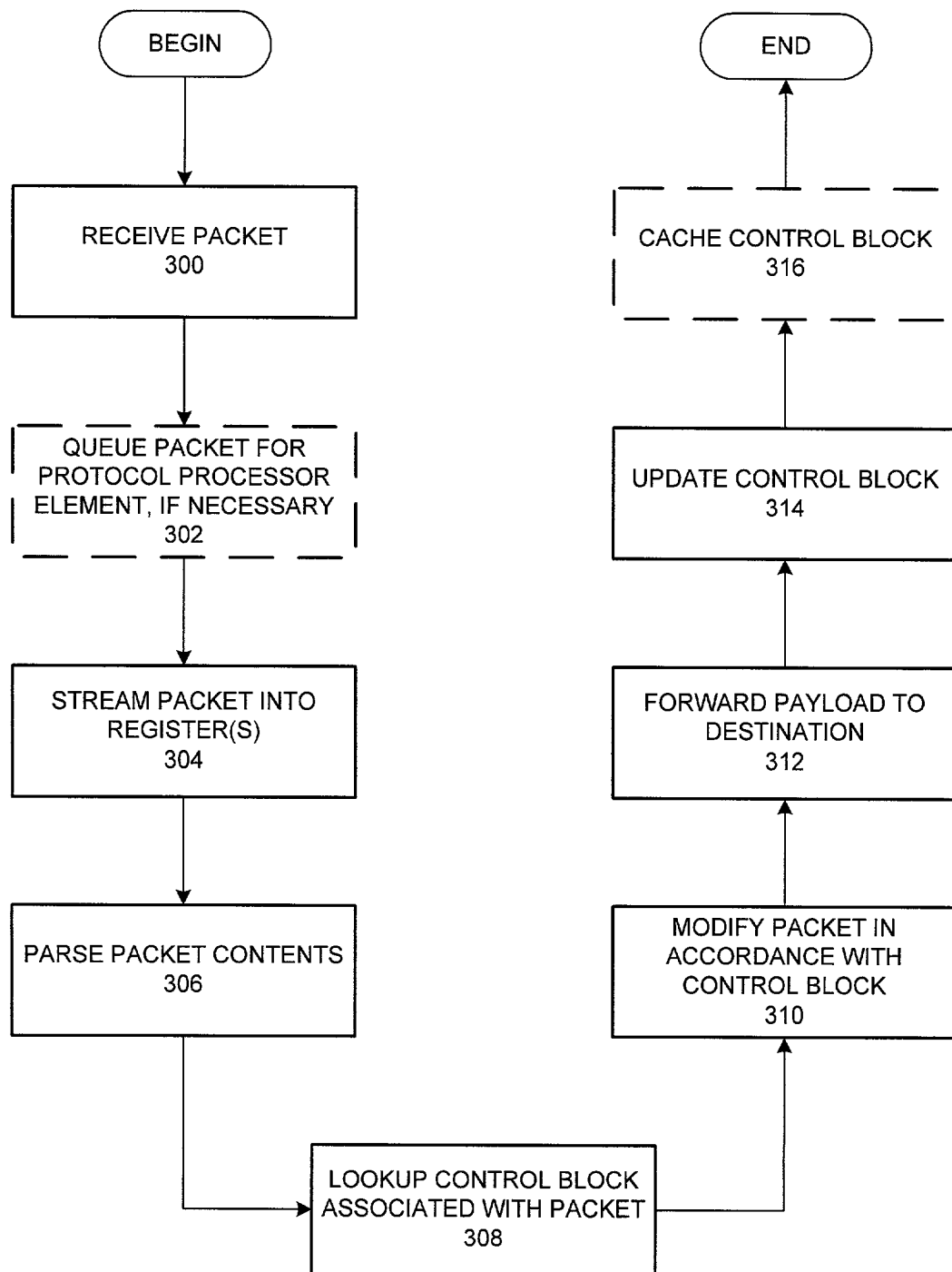


FIG. 3

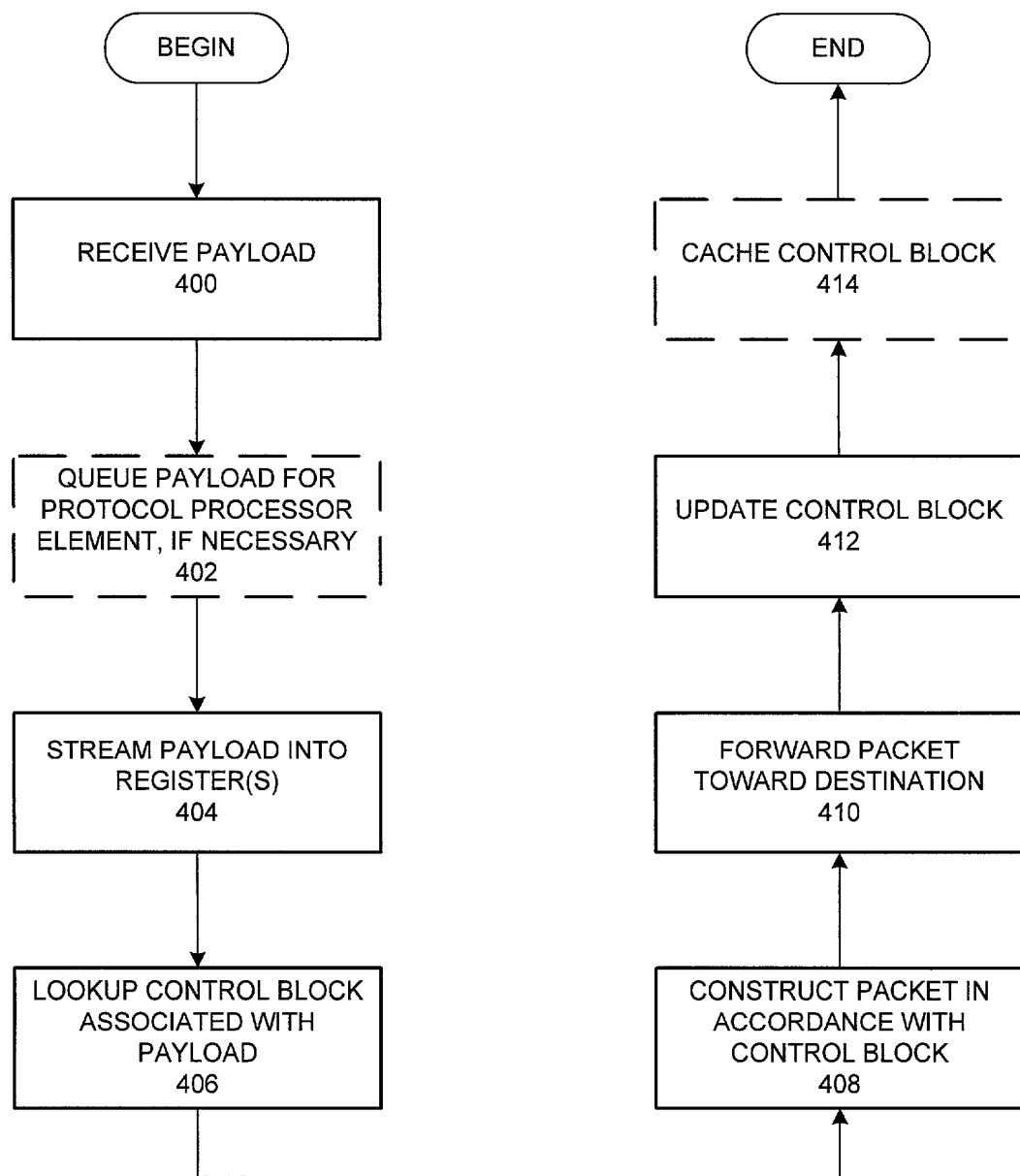


FIG. 4